



FEB. 2007 3:55PM

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NO. 549 P. 2

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SAN FRANCISCO, CA 94105
02/02/2007 TTRAN2 00000022 031952 10799061
01 FC:2501 700.00 DA
02 FC:8001 9.00 DA

Monique Lawson (Depositor's name)
Monique Lawson (Signature)
February 1, 2007 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10799,061	03/12/2004	Eitan Cadouri	524322001200	6747

TITLE OF INVENTION: SELECTING DIE PLACEMENT ON A SEMICONDUCTOR WAFER TO REDUCE TEST TIME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$0	\$0	\$700	02/02/2007
EXAMINER		ART UNIT	CLASS-SUBCLASS			
HOLLINGTON, JERMELE M		2829	324-756000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

PDF Solutions, Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

San Jose, California

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are submitted:

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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Date February 1, 2007

Typed or printed name

Peter J. Yim

Registration No.

44,417

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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FROM: Peter J. Yim
Reg. No.44,417**DATE:** February 1, 2007

Number of pages with cover page:	3	
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Comments:**Please Process the Attached.**U.S. Patent Application Serial No. 10/799,061
For: SELECTING DIE PLACEMENT ON A SEMICONDUCTOR WAFER TO
REDUCE TEST TIME
By: Eitan CADOURI
Our Reference: 52432-20012.00

Attached Is The Following:

PTOL-85 (in duplicate, 2 pages)

sf-1910334